

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/780,030 Confirmation No.: 9259
First Named Inventor: Park, Jongmin Filing Date: 17 February 2004
Group Art Unit: 2827 Examiner: Lam, David
Atty. Docket No.: R-0004 US
Title: Voltage Discharge Technique for Controlling Threshold-voltage
Characteristics of Floating-gate Transistor in Circuitry Such as Flash
EPROM
Assignee(s): ProMOS Technologies Inc.

Mountain View, California
26 April 2005

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, Virginia 22313-1450

PRELIMINARY AMENDMENT TO TEXT

Sir:

Please amend the above patent application in the following manner.

Ronald J. Meetin
Attorney at Law
210 Central Avenue
Mountain View, CA
94043-4869
Tel.: 650-964-9767
Fax: 650-964-9779

05/02/2005 WABDELK1 00000028 502641 10780030
01 FC:1201 400.00 DA
02 FC:1202 700.00 DA